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$\boxtimes$ .	The Commissioner is hereby	authorized to charge payment of any additional filing fees required under 37
	CFR § 1.16 and any patent a	pplication processing fees under 37 CFR § 1.17 associated with this pape
	(including any extension fe-	e required to ensure that this paper is timely filed), of to credit any
	overpayment, to Deposit Acco	unt No. 06-1500. A copy of this Transmittal is enclosed for this purpose.
		Respectfully submitted,
Nove	mber 21, 2003	(-100 XX B) 100

Eric J. Sosenko (Reg. No. 34,440) Attorney/Agent for Applicant

CERTIFICATE	OF MAILING	UNDER 37	C.F.R.	§1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, with sufficient postage, in an envelope addressed to: Commissioner for Patents, U.S. Patent and Trademary Office, P. O. Box 1450, Alexandria, VA 22313-1450, on the below date:

Date: November 21, 2003

Ashley L. Rhoades Name: \_

Signature

**BRINKS HOFER GILSON & LIONE** 

BRINKS H O F E R G I L S O N &LIONE

P.O. Box 10395 Chicago, IL 60610

Date



In re Appin. of: Lau

Appin. No.:

10/087,691

Filed:

March 1, 2002

For:

METHOD OF IMPLEMENTING A

VARIABLE NUMBER OF FILTER SECTIONS FOR

DIGITAL SIGNAL PROCESSING

Attorney Docket No: 10541-1904

Examiner: Unknown

Art Unit: 2644

DEC 0 2 2003

RECEIVED

Commissioner for Patents U.S. Patent and Trademark Office P. O. Box 1450 Alexandria, VA 22313-1450

Technology Center 2600

## **INFORMATION DISCLOSURE STATEMENT**

Sir:

In accordance with the duty of disclosure under 37 C.F.R. §1.56, and more particularly in accordance with 37 C.F.R. §1.97(c), Applicant cites the following references (each listed for the Examiner's convenience on the enclosed Form PTO-1449):

DOCUMENT NUMBER	DATE	NAME
4,453,255	06/05/1984	Tsuchikane et al.

DOCUMENT NUMBER	DATE	COUNTRY
GB 2 377 535	08/01/2003	United Kingdom

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

Gay-Bellile et al. "A reconfigurable superimposed 2D-mesh array for channel equalization", IEEE Int. Symp. Circuits & Systems 2002 (ISCAS 2002), 26-29 May 2002, vol 1, pages I-893 to I-896

A copy of each listed reference for which a copy is required under 37 C.F.R. §1.98(a)(2) is also enclosed. Applicant respectfully solicits the Examiner's consideration of the listed references and entry thereof into the record of this application.

By submitting this Statement, Applicant is attempting to fully comply with the duty of candor and good faith mandated by 37 C.F.R. §1.56. As such, this Statement is not intended to constitute an admission that any of the enclosed references, or other information referred to therein, constitutes "prior art" or is otherwise "material to patentability," as that phrase is defined in 37 C.F.R. §1.56(a).

Applicant hereby certifies that each item of information in this Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Statement. Accordingly, Applicant has calculated no fee to be due in connection with the filing of this Statement. However, the Commissioner is authorized to charge any fee deficiency associated with the filing of this Statement to a deposit account, as authorized in the Transmittal accompanying this Statement.

Respectfully submitted.

November 21, 2003

Date

Eric J. Sosenko (Reg. No. 34,440)

Attomey/Agent for Applicant

Enclosures: Form PTO-1449 (one sheet)

Copies of listed references

FORM PTO-1449	APPLICATION NO.	CASE NO.
RADEMAN	10/087,691	10541-1904
LIST OF PATENTS AND PUBLICATIONS FOR	FILING DATE	ART UNIT
APPLICANT'S INFORMATION DISCLOSURE	March 1, 2002	2644
STATEMENT		1
	APPLICANT(S): Lau	

REFERENCE DE	SIGNATION	U.S. PATEN	T DOCUMENTS			
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS/ SUBCLASS	FILING DATE	
	4,453,255	06/05/1984	Tsuchikane et al.			
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## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS/ SUBCLASS	TRANS YES	LATION NO
	GB 2 377 535	08/01/2003	United Kingdom			
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EXAMINER INITIAL	OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)
	Gay-Bellile et al. "A reconfigurable superimposed 2D-mesh array for channel equalization", IEEE Int. Symp. Circuits & Systems 2002 (ISCAS 2002), 26-29 May 2002, vol 1, pages I-893 to I-896

EXAMINER	DATE CONSIDERED